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In re: application of: Dirk Reese et al.
Application Number: 10/786,370
Filed: February 25, 2004
Confirmation Number: 3407
Title: OVER-VOLTAGE PROTECTION OF INTEGRATED CIRCUIT I/O PINS
Atty Docket Number: 015114-067500US JMZ/djb

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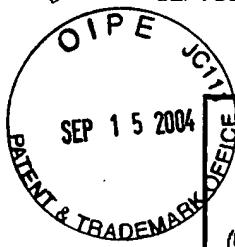
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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application Number	10/786,370
		Filing Date	February 25, 2004
		First Named Inventor	Dirk Reese
		Art Unit	2811
		Examiner Name	
Total Number of Pages in This Submission	4	Attorney Docket Number	015114-067500US

ENCLOSURES (Check all that apply)

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<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input checked="" type="checkbox"/> PTO/SB/81 Power of Attorney (1pg)	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
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<input type="checkbox"/> Response to Missing Parts/ Incomplete Application		
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Remarks

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

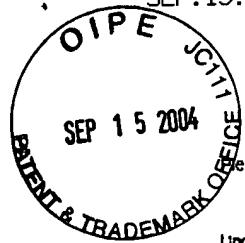
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POWER OF ATTORNEY OR AUTHORIZATION OF AGENT	Application Number	10/786,370
	Filing Date	February 25, 2004
	First Named Inventor	Dirk Reese
	Title	Over-Voltage Protection of Integrated Circuit I/O Pins
	Group Art Unit	2811
	Examiner Name	
Attorney Docket Number	15114-067500US	

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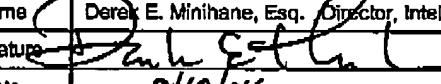
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Applicant/Inventor.
 Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name	Derek E. Minihane, Esq. Director, Intellectual Property Law and Assistant Secretary
Signature	
Date	2/19/04

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Dirk Reese et al.

Application No./Patent No.: 10/786,370 Filed/Issue Date: February 25, 2004

Entitled: Over-Voltage Protection of Integrated Circuit I/O Pins

Altera Corporation, a Corporation
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. the assignee of the entire right, title, and interest; or
2. an assignee of an undivided part interest

In the patent application/patent identified above by virtue of either:

A. An assignment from the Inventor(s) of the patent application/patent identified above. The assignment was recorded in the Patent and Trademark Office at Reel 015059 Frame 0777, or for which a copy thereof is attached.

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Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

2/19/04

Date

Signature

Derek E. Minihane

Typed or printed name

Director, Intellectual Property Law
and Assistant Secretary

Title

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